

**INTEGRATED LIBRARY CORE FOR EMBEDDED PASSIVE
COMPONENTS AND METHOD FOR FORMING
ELECTRONIC DEVICE THEREON**

ABSTRACT

An integrated library core for embedded passive components and a method for forming an electronic device on the library core are provided. An insulating core layer is formed with a plurality of openings penetrating therethrough and with electrically conductive layers on upper and lower surfaces thereof. The openings of the core layer are filled with materials for forming passive components such as resistors and capacitors. This thereby provides an integrated library core on which the electrically conductive layers of the core layer can be desirably patterned to electrically interconnect the passive components, and this library core can be electrically connected to an electronic device such as substrate or printed circuit board to enhance performances of electrical characteristics for the electronic device.

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